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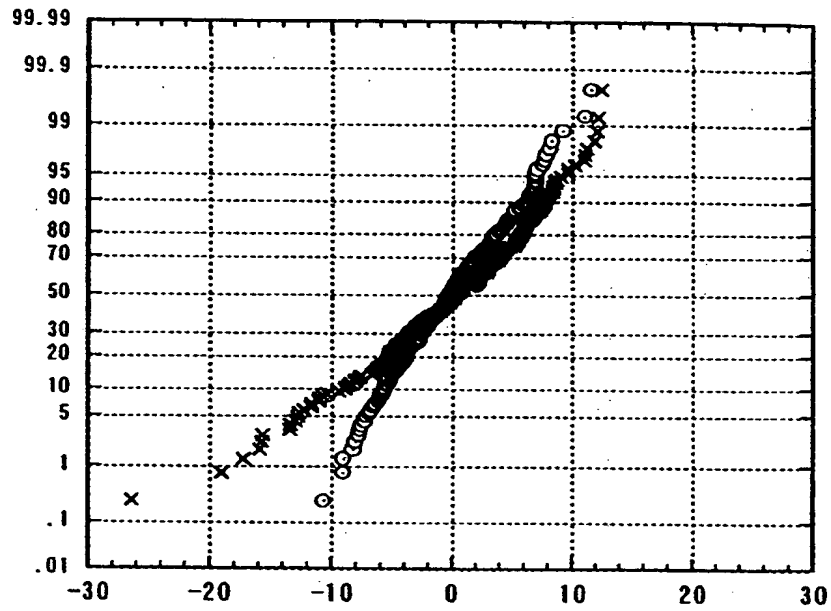
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FIG.1



ON current value fluctuation in adjacent TFTs[%]

FIG.2A

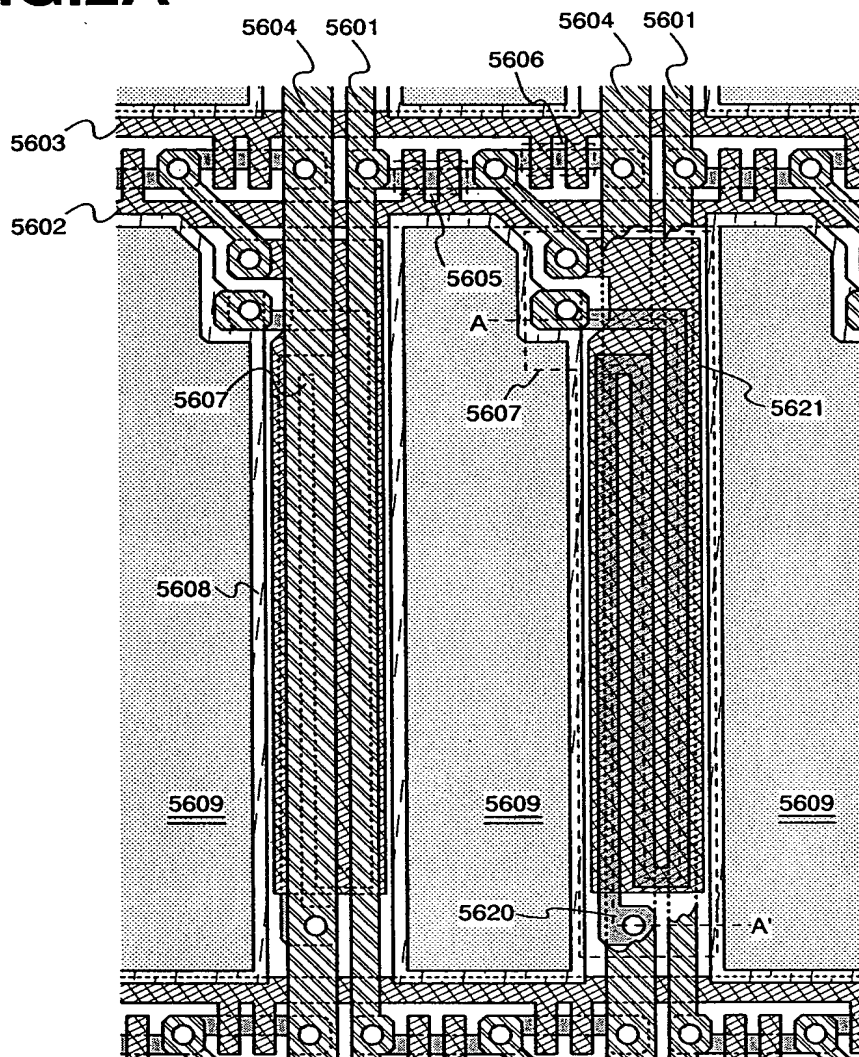


FIG.2B

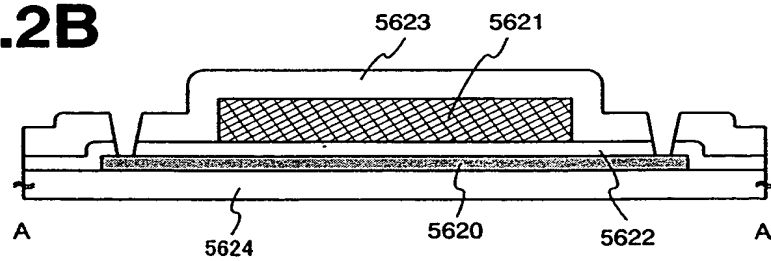


FIG.3A

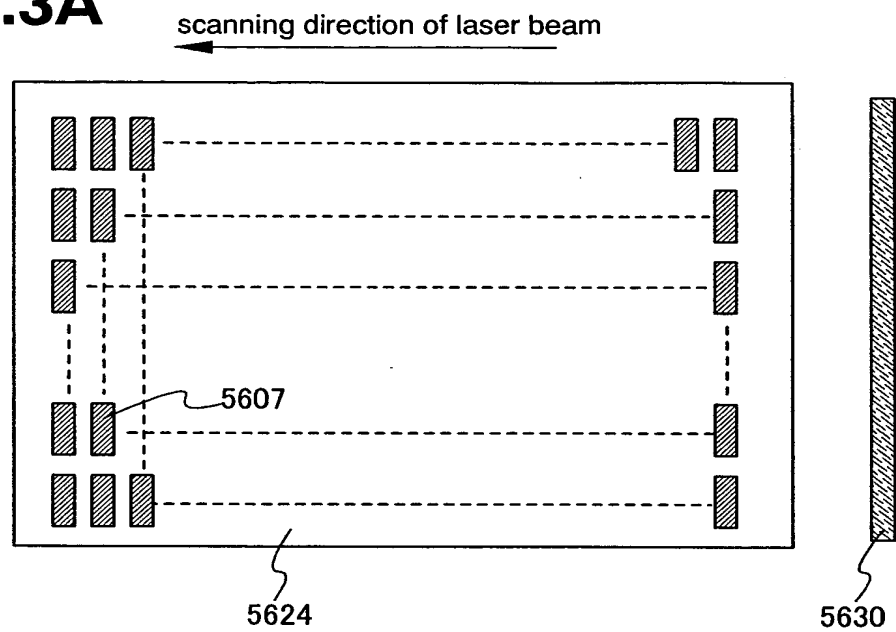


FIG.3B

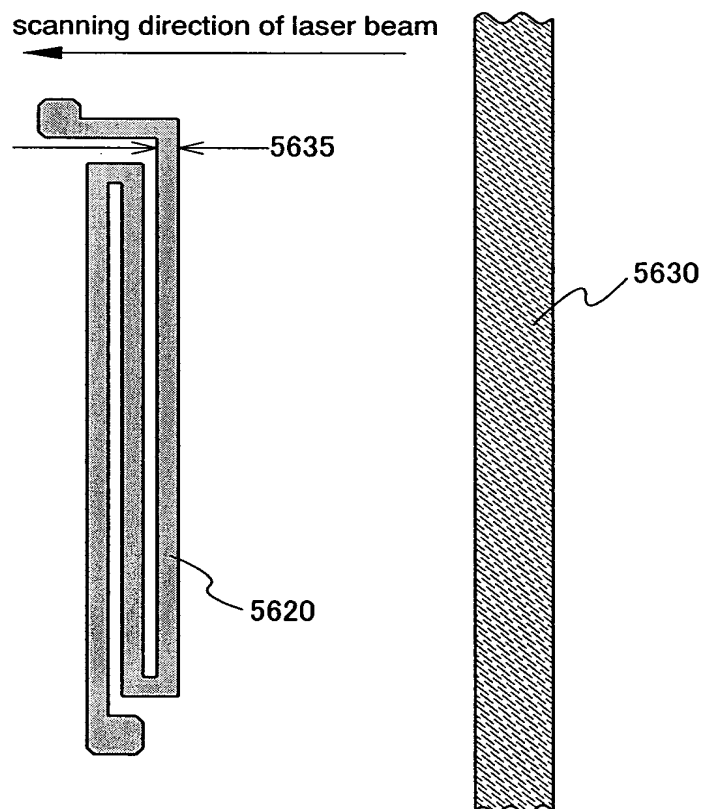


Figure 1 is a graph showing current versus voltage for a PMOS transistor. The y-axis is labeled "current" and the x-axis is labeled "voltage". Four curves are shown, labeled 1511, 1512, 1513, and 1514. The curves are labeled with gate-source voltages: $I_b(V_{GS4})$, $I_b(V_{GS3})$, $I_b(V_{GS2})$, and $I_b(V_{GS1})$. A load line 1515 is shown, intersecting the curves at points 1516, 1517, 1518, and 1519. Below the graph, a circuit diagram shows a PMOS transistor connected to V_{DD} and a load resistor connected to V_{SS} . The gate voltage is V_{GS} , the drain-source voltage is V_{DS} , and the load voltage is V_{EL} .

FIG.5

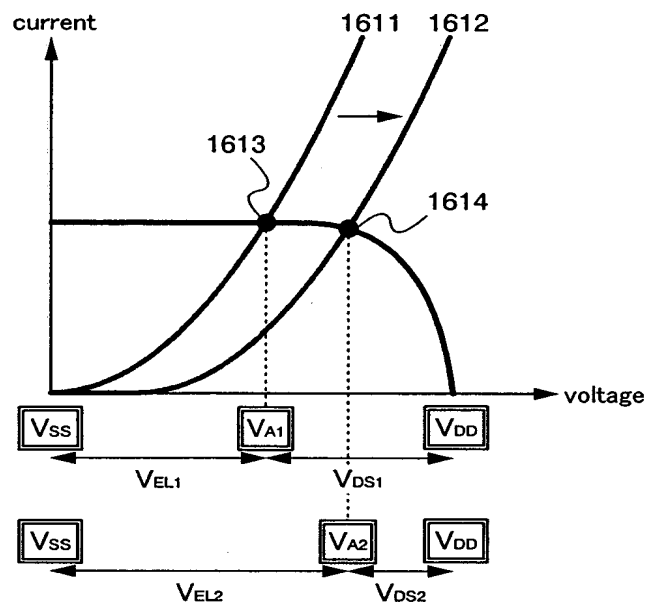


FIG. 6A

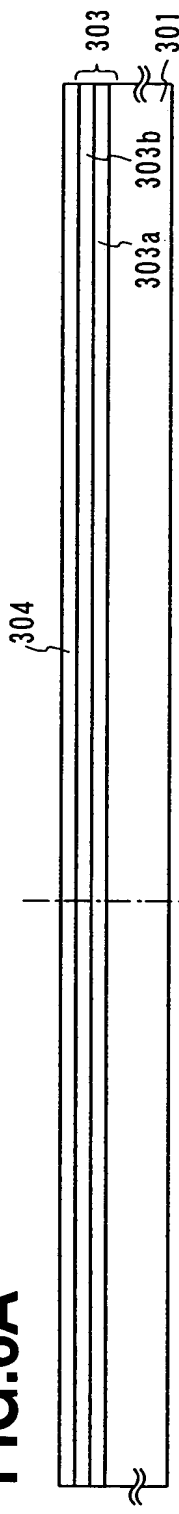


FIG. 6B

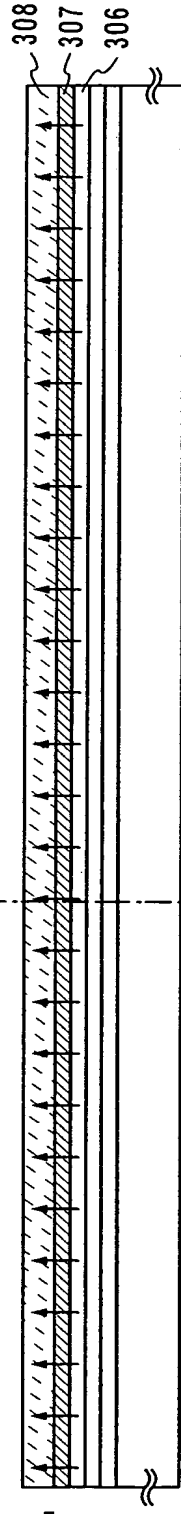


FIG. 6C

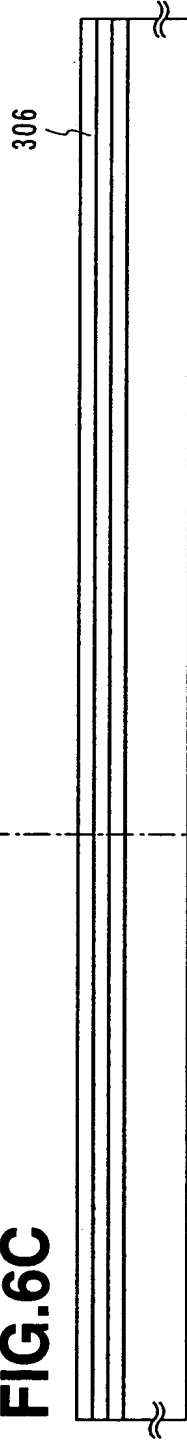


FIG. 6D

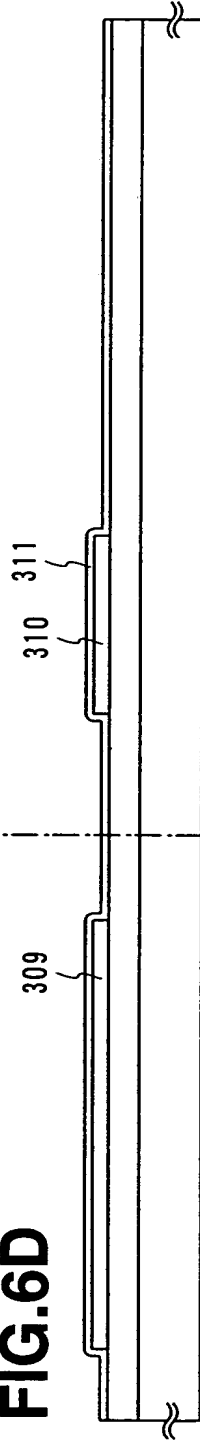


FIG. 6E

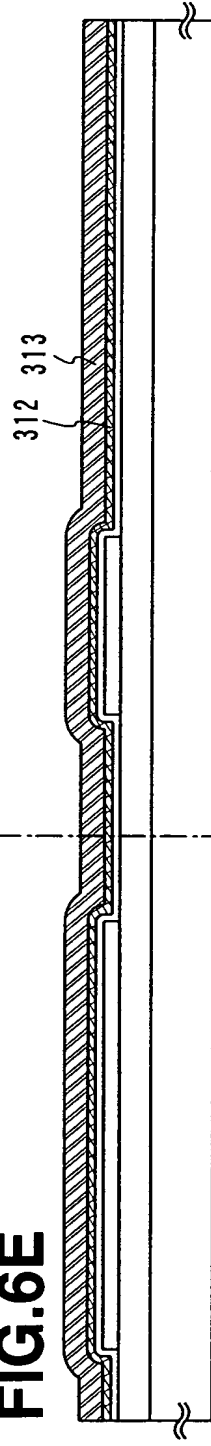


FIG.7A

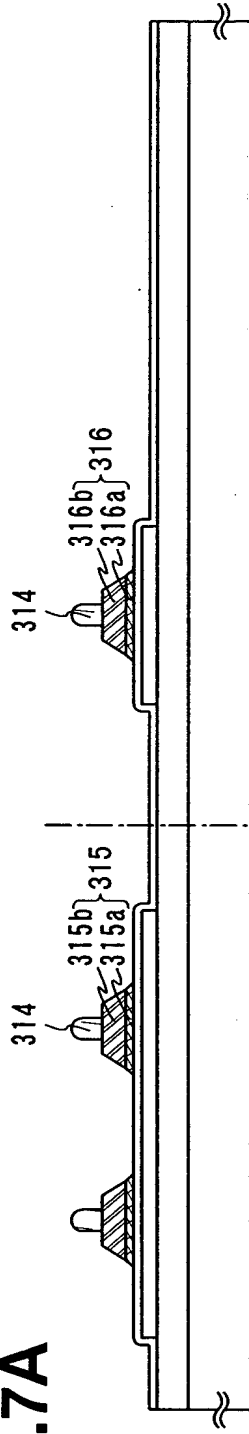


FIG. 7B

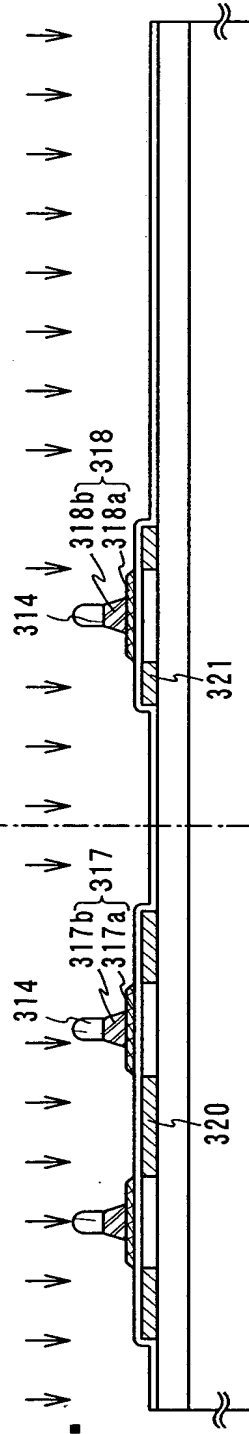


FIG. 7C

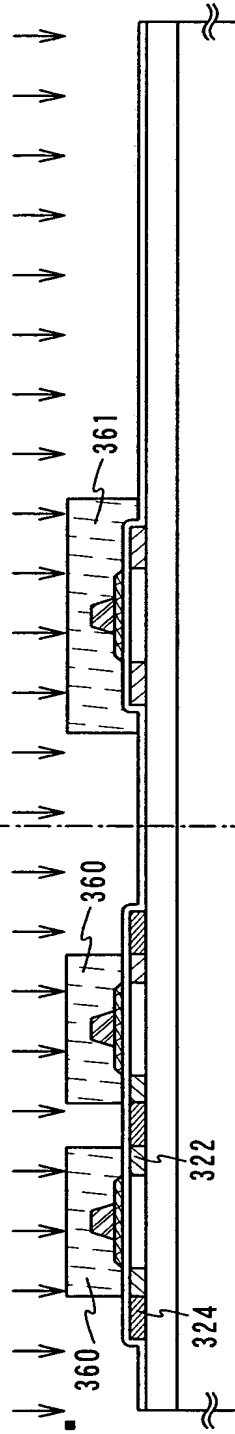


FIG. 7D

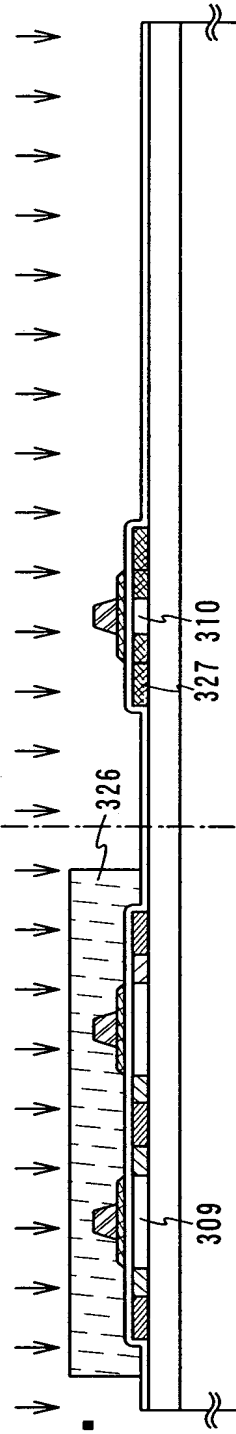


FIG. 8A

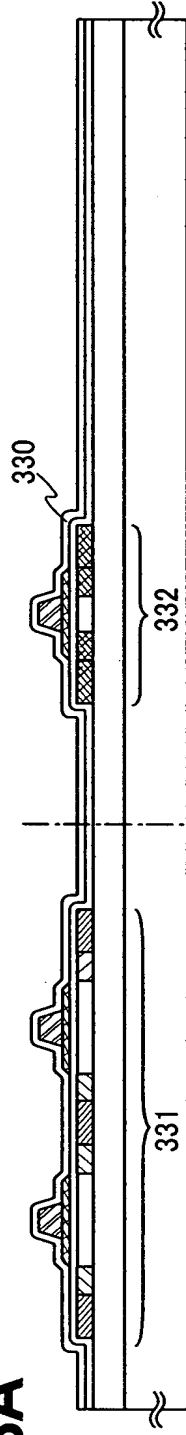


FIG. 8B

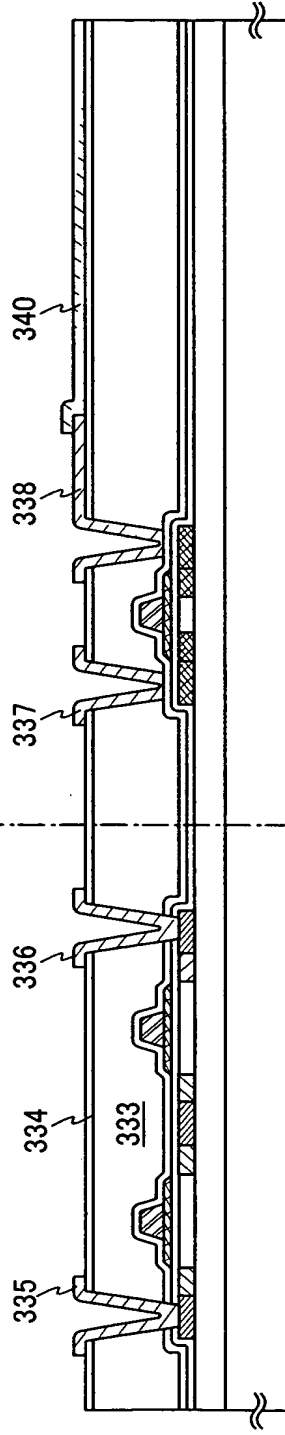


FIG. 8C

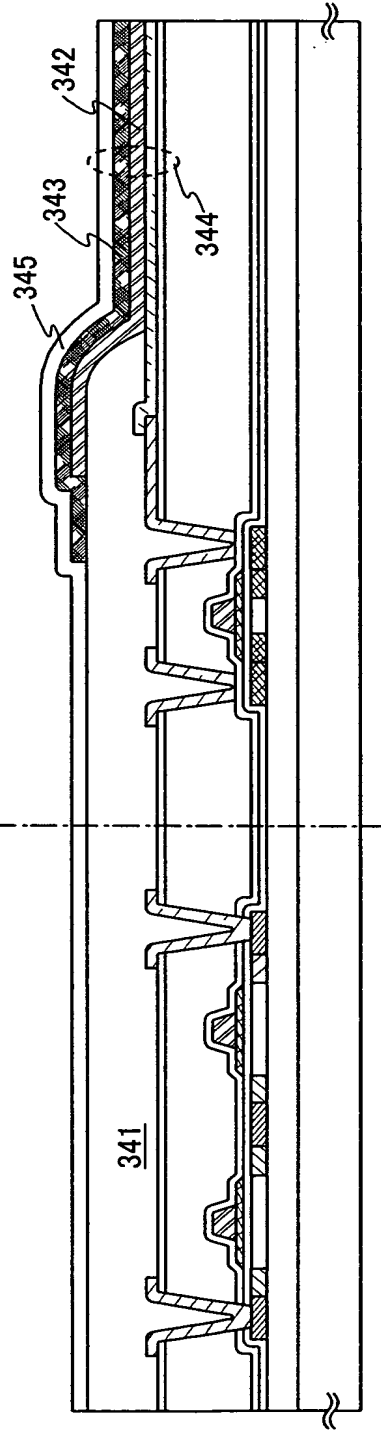


FIG.11A

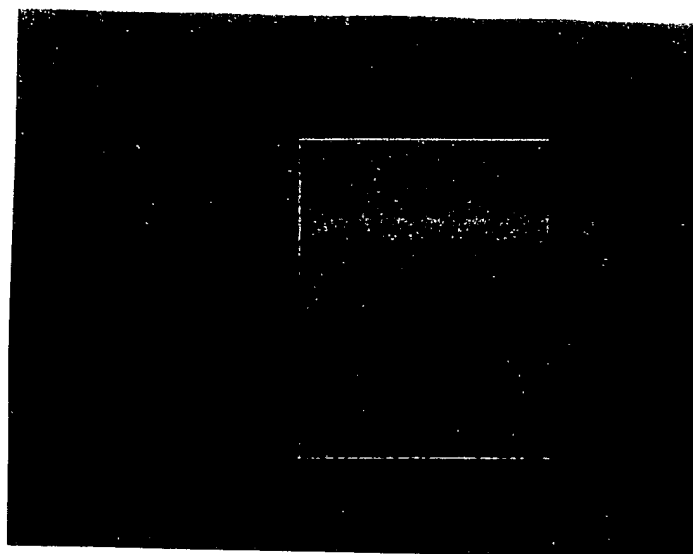


FIG.11B

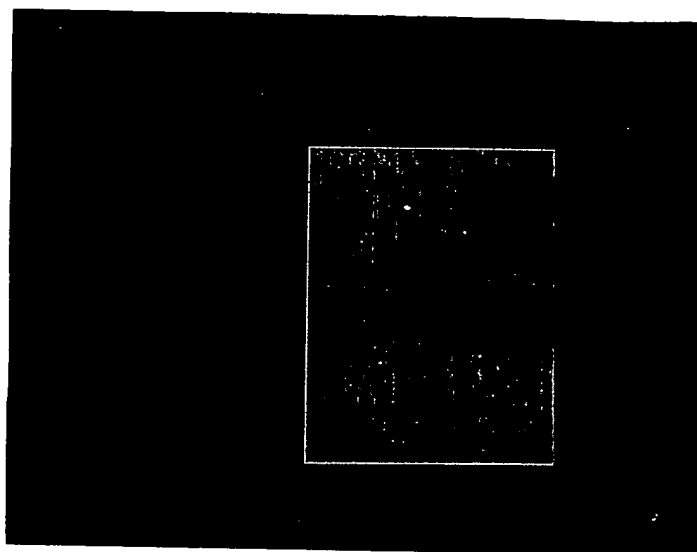


FIG.9

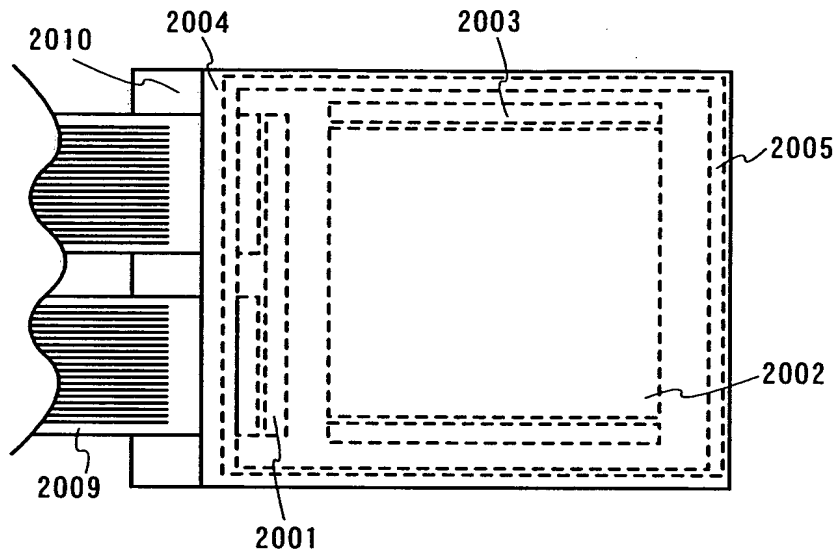


Fig.10A

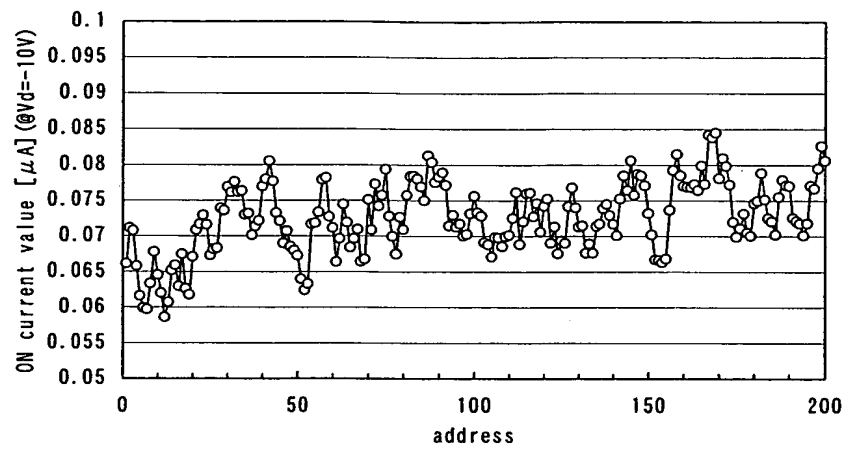


Fig.10B

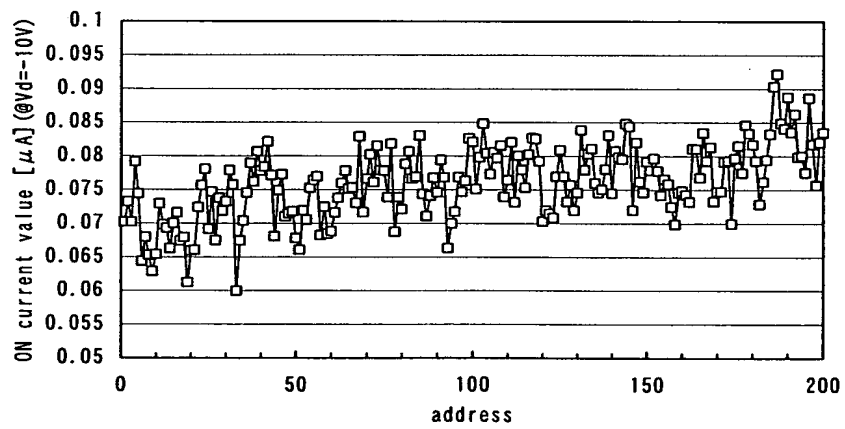


FIG.12A

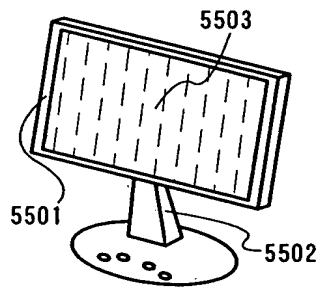


FIG.12B

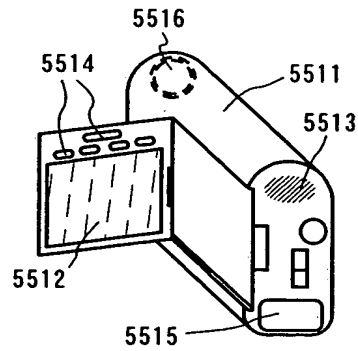


FIG.12C

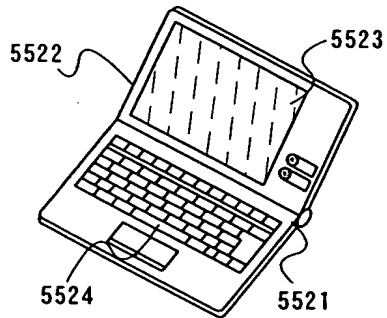


FIG.12D

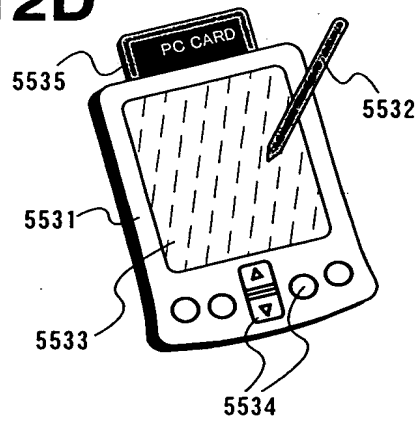


FIG.12E

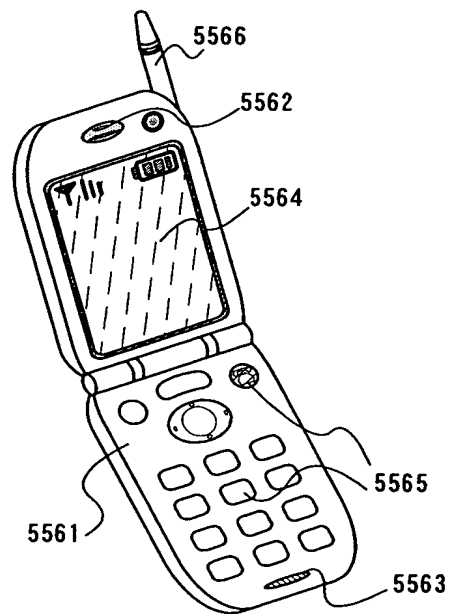
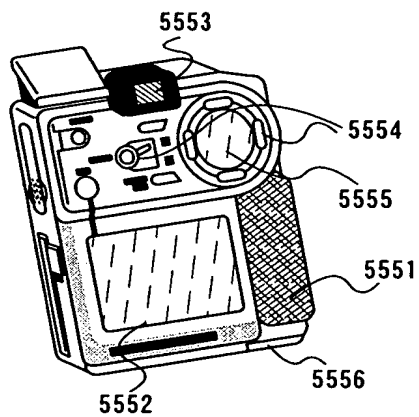


FIG.12F